

N76E003 BSP for KEIL C51 Revision History

Reversion 1.03 (Release 2017-11-2)

- Modify ADC read band-gap and calculate VDD demo.
- Modify UART0 project option setting.
- Modify IAP_Dataflash_EEPROM the XRAM storage address start from 0x280.
- Modify function_define.h file define of Set_All_GPIO_Quasi_Mode P0M2.
- Modify function_define.h file TIMER0_MODE3_ENABLE / TIMER1_MODE3_ENABLE setting.

Reversion 1.02 (Release 2017-6-20)

- Add POR disable instruction in startup.a51
- Add ADC read band-gap and calculate VDD value demo
- Add ADC 16 times optimize demo
- Add read UCID sample code
- Modify Timer 2 capture sample setting.
- Modify I2C EEPROM demo code, add check SI status, modify error check process.

Reversion 1.01 (Release 2017-1-20)

- Fix PWM interrupt initial setting in Function_define.h
- Fix SFR list add P1.6 / P1.7 define in N76E003.h
- Add SPI Flash 25Q16 read / write demo.
- Add SPI master / slave control demo code, include interrupt and polling.

Reversion 1.00 (Release 2016-11-15)

- Initial release version

Appendix: Supported Devices

Supported Device of Nu-link for 8051 Series	
LPC 1T 8051 Product Line	
N76E003 series	N76E003AT20, N76E003AQ20

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