

N76E003 BSP for KEIL C51 Revision History

Reversion 1.05 (Release 2018-1-29)

Modified "Modify IAP_Dataflash_EEPROM" the XRAM code issue and add 16bit combine sample. Fix XRAM address error

Added define UART1 as printf output function sample project.

Removed I2C slave project waiting interrupt stop issue.

Removed "ADC_bandgap_VDD", replaced with "ADC_Bandgap_VDD_noDelay" Project demo for use bandgap value to calculate VDD value.

Reversion 1.04 (Release 2017-12-28)

Modified "Modify IAP_Dataflash_EEPROM" the XRAM code issue and add 16bit combine sample.

Added "Watch dog reset disable" sample project.

Removed SFR_Macro.h set_BOV2 define.

Reversion 1.03 (Release 2017-11-2)

Modified ADC read band-gap and calculate VDD demo.

Modified UART0 project option setting.

Modified IAP_Dataflash_EEPROM the XRAM storage address start from 0x280.

Modified function_define.h file define of Set_All_GPIO_Quasi_Mode P0M2.

Modified function_define.h file TIMER0_MODE3_ENABLE / TIMER1_MODE3_ENABLE setting.

Reversion 1.02 (Release 2017-6-20)

Added POR disable instruction in startup.a51

Added ADC read band-gap and calculate VDD value demo

Added ADC 16 times optimize demo

Add read UCID sample code

Modified Timer 2 capture sample setting.

Modified I2C EEPROM demo code, add check SI status, modify error check process.

Reversion 1.01 (Release 2017-1-20)

Fixed PWM interrupt initial setting in Function_define.h

Fixed SFR list add P1.6 / P1.7 define in N76E003.h

Added SPI Flash 25Q16 read / write demo.

Added SPI master / slave control demo code, include interrupt and polling.

Reversion 1.00 (Release 2016-11-15)

Initial release version

Appendix: Supported Devices

Supported Device of Nu-link for 8051 Series	
LPC 1T 8051 Product Line	
N76E003 series	N76E003AT20, N76E003AS20, N76E003AQ20, N76E003BQ20

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