Power

Debug Interface

LED

Reset

Connect

U2 — <NM18101Y>

Pin	Signal
33	VSS
24	UHO
23	U
22	ULO
21	HV_IO
20	NC
19	PD1 TICECLK
18	PD4 LED1
17	PC0
25	UB
26	VLO
27	V
28	VHO
29	VB
30	WLO
31	W
32	WHO
16	PC3
15	PD3
14	PD2 TICEDAT
13	PC2
12	PB3
11	nRESET TICERST
10	PC1
9	PB4
1	WB
2	PLDO
3	VIN
4	VSS
5	DLDO
6	PB0
7	PB1
8	PB2

R87 5.1R
C17 1u/50V
R86 5.1R
C18 1u/50V
R85 5.1R
C19 1u/50V
C16 10u
C15 1u

Power section:
VIN
C8 10uF/35V
C9 0.1u
VDD J2 1 2 3
J4 1 2 3
VIN_ J5 1 2 3

Debug Interface:
JP4
1 2
3 4
5 6
7 8
9 10
TX0
RX0
TICEDAT
TICECLK
TICERST
VIN

LED:
GPIO1 RED N3 R9 1K VDD LED1
POWER1 GREEN N1 R10 1K VDD

Reset:
VDD R8 100K
SW1 PUSH BOTTOM (B)
TICERST
C14 1uF/16V

Connect:
JP8 HEADER 15
1	VIN_	R84 33R(0) VIN
2	PB0	Bemf_U
3	PB1	Bemf_V
4	PB2	Bemf_W
5	PB4	
6	PC1	OCP
7		
8	PB3	PGAI
9	PC2	Speed_Command
10	PD2	
11	PD3	
12	PC3	PGAO
13	PC0	
14	PD4	LED
15	GND	

JP7 HEADER 15
1	WB
2	WHO
3	W
4	WLO
5	VB
6	VHO
7	V
8	VLO
9	UB
10	UHO
11	U
12	ULO
13	PD6 RX0
14	PD5 TX0
15	PD1